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**Complete if Known**

Application Number	10/824,623
Filing Date	April 15, 2004
First Named Inventor	Mark F. Bocko
Art Unit	2849 2816
Examiner Name	Unassigned
Attorney Docket Number	000687-00335

Sheet 1 of 1

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
n	01	"A Low Noise Chopper-Stabilized Differential Switched-Capacitor Filtering Technique", K. Hsieh, et al., IEEE Journal of Solid-State Circuits, Vol. SC-16, No. 6, December 1981	
n	02	"A 160-MHz Fourth-Order Double-Sampled SC Bandpass Sigma-Delta Modulator", S. Bazarjani, et al., IEEE Transaction on Circuits and Systems, II: Analog and Digital Signal Processing, Vol. 45, No. 5, May 1998	
n	03	"Circuit Techniques for Reducing the Effects of Op-Amp Imperfections: Autozeroing, Correlated Double Sampling, and Chopper Stabilization", C. Enz, et al., Proceedings of the IEEE, Vol. 84, No. 11, Nov. 1996	
n	04	"Bandpass Analog-To-Digital Conversion for Wireless Applications", A. Ong, September 1998, Stanford University, Stanford	

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